

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,972,597 B2
APPLICATION NO. : 10/748748
DATED : December 6, 2005
INVENTOR(S) : Kim

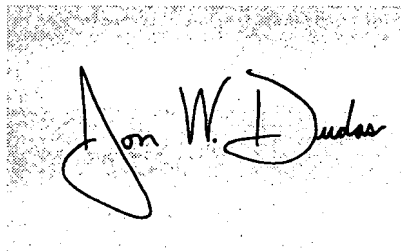
Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At column 1, line 61 replace "V₁" with --V_g--
At column 1, line 63, replace "V₁" with --V_g--
At column 1, line 65 replace "V₁" with --V_g--
At column 8, line 3 replace "12" with --I2--
At column 8, line 16 replace "N1." with --N10.--
At column 8, line 55 replace "13" with --I3--
At column 9, line 66 replace "field-effect of transistors" with --field-effect transistors of--
At column 11, line 34 replace "receive the" with --receives the--
At column 12, line 16 replace "current supplying" with --current; supplying--

Signed and Sealed this

Eighth Day of May, 2007

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized, with a large, looped "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office